



TECHNOLOGY SUMMARY

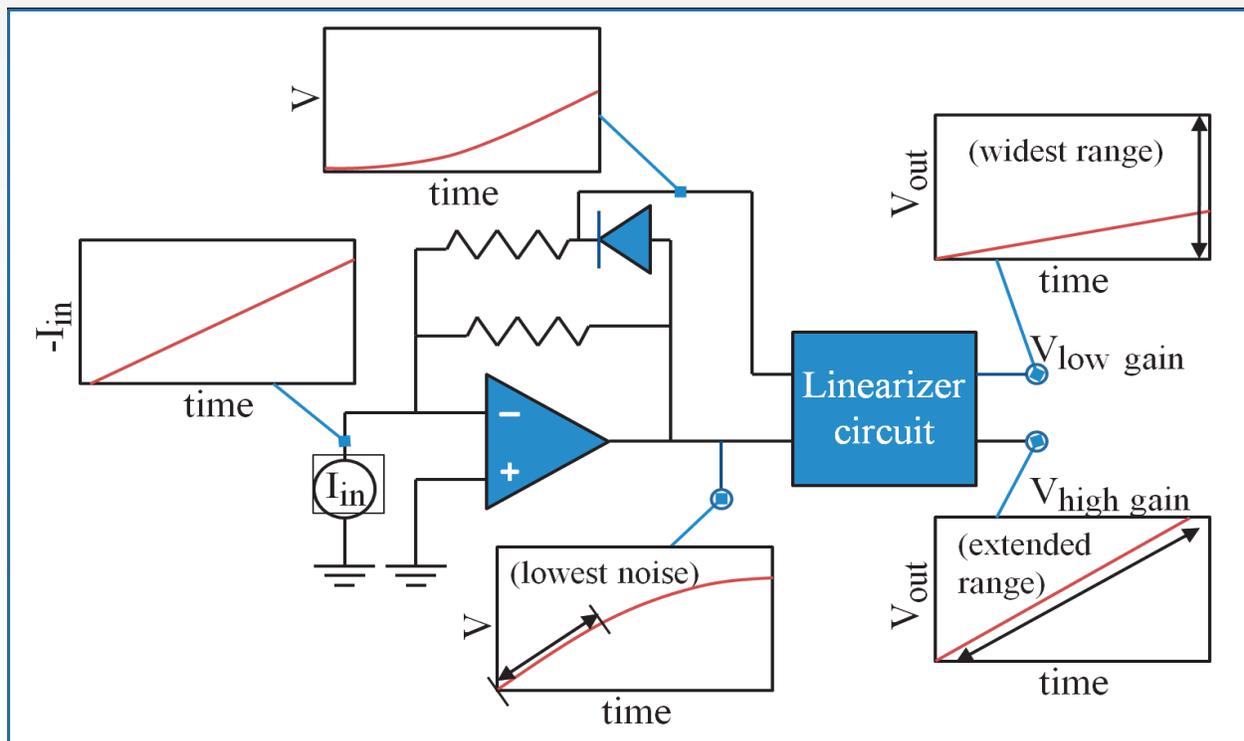
US PATENT # 7,825,735

TECHNOLOGY READINESS LEVEL: 6

REPRESENTATIVE LABORATORY PROTOTYPE HAS BEEN DEMONSTRATED IN APPLICATION-RELEVANT ENVIRONMENTS.

Many applications require wide range detection, where detector current is converted to a voltage by a trans impedance amplifier (TIA). High gain TIAs provide low noise but easily saturate. Logarithmic TIAs provide wide range, but nonlinear response. Auto-ranging circuits lose data when switching gain. Range is especially a challenge for modern low supply voltage ICs, where to maintain adequate resolution over a wide range the engineer has to reduce TIA gain, fight a rising noise floor, and add costly bits the analog digital converter (ADC).

To solve this problem Sandia engineers developed a hybrid TIA that combines a high gain and logarithmic amplifier in one. The hybrid TIA cleverly utilizes nonlinearity to provide wide range sensitivity, but conveniently internally linearizes the outputs for further in-line amplification or datalogging. With two outputs to chose from, an ADC may record data from the low noise output as signal rises, then switch to recording the wide range output with nanosecond digital logic speed, effectively extending the ADC's range, while preserving signal linearity and data fidelity.





POTENTIAL APPLICATIONS

- Photodetectors
- X-Ray/Gamma & Radiation Detection
- Optical Power Meters
- Fiber Alignment
- Characterizing Laser Dynamics (threshold, pre/post pulse)
- High Dynamic Range, High Speed Current Sensing

TECHNOLOGICAL BENEFITS

- Log amplifier-like range with linear outputs
- No data dropouts for auto-ranging
- Simultaneous low noise and wide-range outputs
- Best reliability for unknown signal strength
- Single front-end amplifier with greater survivability

CONTACT INFORMATION

For more information or to discuss licensing opportunities contact us at

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Refer to SD # 11248

or visit

<https://ip.sandia.gov>